

AMENDMENT TO THE SPECIFICATION

Please replace the first full paragraph at page 4, with the following rewritten paragraph:

-- Several proposals are made about a digital phase control circuit for digitally controlling, by using delay locked loops and selection circuits, phases of non-uniform multi-phase clock signals at a resolution shorter than a propagation delay of a delay buffer in the delay locked loops. For example, a digital phase control circuit is revealed in United States Patent Application Serial No. ~~09/921,866~~ 6,483,360 B2 filed Aug. 3, 2001, by Satoshi Nakamura (this inventor), for assignment to the present assignee, based on Japanese Patent Application No. 237,458 of 2000. The digital phase control circuit disclosed by Nakamura infinitely (cyclically) phase shifts (phase controls), by using a first delay locked loop (DLL) comprising a plurality of chained delay buffers each having a first delay of 229.6 ps, a second delay locked loop (DLL) comprising a plurality of chained delay buffers each having a second delay of 200 ps, and selection circuits, sixteen multi-phase clock signals both in a lead direction and in a lag direction at resolution of 28.6 ps as a whole with a phase interval thereof maintained to keep 200 ps.

--

Please replace the second full paragraph at page 13, with the following rewritten paragraph:

-- In the 2-times oversampling, two clock edges $\pm \frac{1}{2}$ match with one bit of the serial input data i. That is, in the 2-times oversampling, a phase interval between clock signals may be set in one-second of a length of one bit of the serial input data i. For example, it will be assumed that the data rate is equal to 2.5 Gbps. In this event, inasmuch as one bit of the serial input data has 400 ps in length, the phase interval is set in $(400 \text{ ps}/2)$ or 200 ps. The phase interval of 200 ps may be sufficiently implemented. The 2-times oversampling may be implemented at an existing

technical level for the input data i having the data rate not only of hundreds of Mbps but also of several Gbps.

--